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UNIVERSITÄT ZU LÜBECK



**FM**

2023 LÜBECK

# FM 2023 Program

**March 6th - 10th 2023 in Lübeck, Germany**

Sponsored by





# Monday, March 6th

09:00-10:30	DOCTORAL SYMPOSIUM Room: <a href="#">AM2</a>	T1 Paolo Arcaini <b>Hybrid System Falsification Tutorial</b>  Room: <a href="#">AM S3</a> Chair: <b>Martin Sachenbacher</b>	T2 Daniel Neider <b>Verification of Deep Neural Networks</b>  Room: <a href="#">AM3</a> Chair: <b>Hannes Kallwies</b>	T4 Adrián Rebola-Pardo <b>Reasoning with Quantified Boolean Formulas</b>  Room: <a href="#">AM S1</a> Chair: <b>Cesar Sanchez</b>	WORKSHOP <b>Application of formal methods and digital twins</b> Room: <a href="#">AM S2</a>	WORKSHOP FMTea 2023 Room: <a href="#">AM4</a>	
	Invited Inspiration Talk: Paula Herber				Session 1: Session Chair: <b>Stefan Hallerstede</b>	Welcome from the chairs	
	Julius Adelt: Reusable Contracts for Deductive Verification of Autonomous Hybrid Systems				Welcome	Keynote: Erika Ábrahám - Automated Exercise Generation for Satisfiability Checking	
10:30-11:00	Coffee Break						
11:00-12:30	Thomas Flinkow: Modular Neural Network Verification	T1 Paolo Arcaini <b>Hybrid System Falsification Tutorial</b>  Room: <a href="#">AM S3</a> Chair: <b>Martin Sachenbacher</b>	T2 Daniel Neider <b>Verification of Deep Neural Networks</b>  Room: <a href="#">AM3</a> Chair: <b>Hannes Kallwies</b>	T4 Adrián Rebola-Pardo <b>Reasoning with Quantified Boolean Formulas</b>  Room: <a href="#">AM S1</a> Chair: <b>Cesar Sanchez</b>	Sylvain Hallé, Chukri Soueidi and Yliès Falcone: <b>Leveraging Runtime Verification for the Monitoring of Digital Twins</b>  Eduard Kamburjan, Vidar Klungre, Silvia Lizeth Tapia Tarifa, Rudolf Schlatte, Martin Giese, David Cameron and Einar Broch Johnsen: <b>Emerging Challenges in Compositionality and Correctness for Digital Twins</b>  Jonas Schiffl and Alexander Weigl: <b>Are Formal Contracts a useful Digital Twin of Software Systems?</b>	Thierry Lecomte: <b>Teaching and Training in Formalisation with B</b>	
	Marco Sälzer: Fundamental Limits of Formal Verification of Deep Neural Networks					Nicolas Féral and Alain Giorgetti: <b>A Gentle Introduction to Verification of Parameterized Reactive Systems</b>	
	Bas van den Heuvel: Session Types for Correct Communicating Software Systems					Géraldine Brieven, Liénardy Simon, Lev Malcev and Benoit Donnet: <b>Graphical Loop Invariant Based Programming.</b>	
	Anna Fritz: Negotiating Remote Attestation Protocols						
Dara MacConville: A Robotics Focused Verification Tool for Python							
12:30-14:00	Lunch						
14:00-15:30	Andoni Rodriguez: Quantifier-elimination methods for faster reactive synthesis modulo theories		T3 Nathanaël Fijalkow <b>Machine Learning Guided Program Synthesis</b>  Room: <a href="#">AM3</a> Chair: <b>Aliyu Ali</b>	T4 Adrián Rebola-Pardo <b>Reasoning with Quantified Boolean Formulas</b>  Room: <a href="#">AM S1</a> Chair: <b>Cesar Sanchez</b>	Martin Leucker, Martin Sachenbacher and Lars Bernd Vosteen: <b>Digital Twin for Rescue Missions – a Case Study</b>  Daniel Thoma, Martin Sachenbacher, Martin Leucker and Aliyu Tanko Ali: <b>A Digital Twin for Coupling Mobility and Energy Optimization: The ReNuBiL Living Lab</b>  Andrea Pferscher, Benjamin Wunderling, Bernhard K. Aichernig and Edi Muskardin: <b>Mining Digital Twins of a VPN Server</b>	Felix Freiberger: <b>Model Checking Concurrent Programs for Autograding in pseuCo Book</b>	
	Daniel Jurjo: Defining abstract domains using rewriting rules					Somsak Vanit-Anunchai: <b>Teaching low-code Formal Methods with Coloured Petri Nets</b>	
	Benjamin Bisping: Analyzing Equivalences in Concurrent Models					Markus Alexander Kuppe: <b>Teaching TLA+ to Engineers at Microsoft</b>	
	Muhammad Rizwan Ali: Cost Analysis for a Resource Sensitive Workflow Modelling Language						
15:30-16:00	Coffee Break						
16:00-17:30	Stefan Marksteiner: Learning-Based Model-Driven Cybersecurity Test Case Generation		T3 Nathanaël Fijalkow <b>Machine Learning Guided Program Synthesis</b>  Room: <a href="#">AM3</a> Chair: <b>Aliyu Ali</b>	T4 Adrián Rebola-Pardo <b>Reasoning with Quantified Boolean Formulas</b>  Room: <a href="#">AM S1</a> Chair: <b>Cesar Sanchez</b>	Session 4: Session Chair: <b>Eduard Kamburjan</b>	Special session: Formal Methods in the ACM curriculum	
	Lex Bailey: Checking Confidentiality in Isabelle/UTP						Digital Twin Workshop Discussion
	Surasak Phetmanee: Towards Verifying Stackelberg Security Games						
	Jonas Schiffl: An Abstract Representation for Designing and Verifying Smart Contracts						
Carolina Gerlach: Probabilistic Hyperproperties							
Welcome Reception (City Hall of Lübeck)							

# Tuesday, March 7th

\* Nominated for Best Paper Award

08:45-09:00	Opening Session
09:00-10:00	Session 0 Session Chair: Joost-Pieter Katoen
	Keynote Talk Room: AM1 Laura Kovacs - Symbolic Computation in Automated Program Reasoning
10:00-10:30	Ana Cavalcanti - Lucas Award
10:30-11:00	Coffee Break
11:00-12:30	Session: SAT/SMT Session Chair: Martin Sachenbacher
	Sylvie Boldo, Francois Clement, Vincent Martin, Micaela Mayero and Houda Mouhcine
	<b>A Coq formalization of Lebesgue Induction Principle and Tonelli's Theorem</b>
	Tomáš Kolárik and Stefan Ratschan
	<b>Railway Scheduling Using Boolean Satisfiability Modulo Simulations</b>
12:30-14:00	Matan Peled, Bat-Chen Rothenberg and Shachar Itzhaky
	<b>SMT Sampling via Model-Guided Approximation *</b>
	Yu Liu, Pavle Subotic, Emmanuel Letier, Sergey Mechtaev and Abhik Roychoudhury.
	<b>Efficient SMT-based Network Fault Tolerance Verification</b>
12:30-14:00	Lunch   FME Business Meeting - Room: AM S2
14:00-15:30	Session: Verification 1 Session Chair: Sandrine Blazy
	Robert Sison, Scott Buckley, Toby Murray, Gerwin Klein and Gernot Heiser
	<b>Formalising the Prevention of Microarchitectural Timing Channels by Operating Systems</b>
	Maurice H. ter Beek, Guillermina Cledou, Rolf Hennicker and José Proença
	<b>Can we Communicate? Using Dynamic Logic to Verify Team Automata</b>
15:30-16:00	Gianluca Amato and Francesca Scozzari
	<b>The ScalaFix equation solver</b>
	Huanhuan Sheng, Alexander Bentkamp and Bohua Zhan
	<b>HHLPy: Practical Verification of Hybrid Systems using Hoare Logic</b>
16:00-17:20	Coffee Break
16:00-17:20	Session: Quantitative Verification Session Chair: Matthias Volk
	Fabian Bauer-Marquart, Stefan Leue and Christian Schilling
	<b>symQV: Automated Symbolic Verification of Quantum Programs</b>
	Stefano M. Nicoletti, Milan Lopuhaä-Zwakenberg, E. Moritz Hahn and Mariëlle Stoelinga
	<b>PFL: a Probabilistic Logic for Fault Trees</b>
Sven Dziadek, Uli Fahrenberg and Philipp Schlehuber-Caissier	
<b>Energy Problems in Finite and Timed Automata with Büchi Conditions *</b>	
Rubén Rubio, Narciso Marti-Oliet, Isabel Pita and Alberto Verdejo	
<b>QMaude: quantitative specification and verification in rewriting logic</b>	



# Wednesday, March 8th

\* Nominated for Best Paper Award

	<b>Session 0</b> Session Chair: <b>Martin Leucker</b>	
09:00-10:00	<b>Keynote Talk</b> Room: <a href="#">AM1</a> Harald Rueß - The next big thing: from embedded systems to embodied actors	
10:00-10:20	<b>Journal First Track</b> Room: <a href="#">AM1</a> Angelo Ferrando, Rafael C. Cardoso, Marie Farrell, Matt Luckcuck, Fabio Papacchini, Michael Fisher and Viviana Mascardi <b>Bridging the gap between single- and multi-model predictive runtime verification</b>	
10:20-10:30	Sebastian Jester: <b>Research Program Funding - Cybersecurity</b>	
10:30-11:00	Coffee Break & Exhibition in Foyer	
11:00-12:30	<b>MAIN SYMPOSIUM</b> Room: <a href="#">AM1</a>	<b>INDUSTRY DAY</b> Room: <a href="#">AM4</a>
	Session: <b>Concurrency and Memory Models</b> Session Chair: <b>Marieke Huisman</b>	Session: <b>Industry Day 1</b> Session Chair: <b>Chih-Hong-Cheng</b>
	Vincenzo Ciancia , Jan Friso Groote, Diego Latella, Mieke Massink and Erik De Vink	Franck Cassez , Joanne Fuller, Milad K. Ghale, David Pearce and Horacio Mijail Anton Quiles
	<b>Minimisation of Spatial Models using Branching Bisimilarity</b>	<b>Formal and Executable Semantics of the Ethereum Virtual Machine in Dafny</b>
	Heike Wehrheim, Lara Bargmann and Brijesh Dongol <b>Reasoning about Promises in Weak Memory Models with Event Structures</b>	Ben Liblit, Linghui Luo, Alejandro Molina, Rajdeep Mukherjee, Zachary Patterson, Goran Piskachev, Martin Schäfer, Omer Tripp and Willem Visser <b>Shifting Left for Early Detection of Machine-Learning Bugs</b>
	Robert Colvin <b>A fine-grained semantics for arrays and pointers under weak memory models</b>	Masoud Ebrahimi, Stefan Marksteiner, Dejan Nickovic, Roderick Bloem, David Schögler, Philipp Eisner, Samuel Sprung, Thomas Schober, Sebastian Chlup, Christoph Schmittner and Sandra König <b>A Systematic Approach to Automotive Security</b>
Petra van den Bos and Sung-Shik Jongmans <b>VeyMont: Parallelising Verified Programs instead of Verifying Parallel Programs</b>	Adam Molin, Edgar Aguilar, Dejan Nickovic, Mengjia Zhu, Alberto Bemporad and Hasan Esen <b>Specification-Guided Critical Scenario Identification for Automated Driving</b>	
12:30-14:00	Lunch & Exhibition in Foyer	
14:00-15:40	Session: <b>Verification 2</b> Session Chair: <b>Volker Stolz</b>	Session: <b>Industry Day 2</b> Session Chair: <b>Thomas Santen</b>
	Marco Paganoni and Carlo A. Furia <b>Verifying At the Level of Java Bytecode</b>	Vahid Hashemi, Jan Kretinsky, Sabine Rieder and Jessica Schmidt <b>Runtime Monitoring for Out-of-Distribution Detection in Object Detection Neural Networks</b>
	Jan Oliver Ringert and Allison K. Sullivan <b>Abstract Alloy Instances</b>	Akshay Dhonthi, Ernst Moritz Hahn and Vahid Hashemi <b>Backdoor Mitigation in Deep Neural Networks via Strategic Retraining</b>

14:00-15:40	David Basin, Daniel Stefan Dietiker, Srdjan Krstic, Yvonne-Anne Pignolet, Martin Raszyk, Joshua Schneider and Arshavir Ter-Gabrielyan <b>Monitoring the Internet Computer</b>	Guy Amir, Ziv Freund, Guy Katz, Elad Mandelbaum and Idan Refaeli <b>veriFIRE: Verifying an Industrial, Learning-Based Wildfire Detection System</b>
	František Blahoudek, Yu-Fang Chen, David Chocholatý, Vojtěch Havlena, Lukáš Holík, Ondrej Lengal and Juraj Síč <b>Word Equations in Synergy with Regular Constraints *</b>	Jan Lukas Deichmann <b>Verification of distributed medical device communication (IEEE 11073 SDC)</b>
	<b>Journal First Track</b> Hichem Debbi <b>A Debugging Game for Probabilistic Models</b>	Falak Sher, Matthias Volk, Marielle Stoelinga and Joost-Pieter Katoen <b>The Fault Tree Analyser SAFEST</b>
<b>Excursion &amp; Conference Dinner</b>		



# Thursday, March 9th

\* Nominated for Best Paper Award

Room: AM1

	<p align="center"><b>Session 0</b> Session Chair: Joost-Pieter Katoen</p>
09:00-10:00	<p align="center"><b>Keynote Talk</b> Room: <u>AM1</u> Nils Jansen <b>Intelligent and Dependable Decision-Making Under Uncertainty</b></p>
10:00-10:20	<p align="center"><b>Journal First Track</b> Room: <u>AM1</u> Nicholas Coughlin and Graeme Smith <b>Compositional noninterference on hardware weak memory models</b></p>
10:30-11:00	<b>Coffee Break</b>
	<p align="center">Session: <b>Formal Methods in AI</b> Session Chair: <b>Özgür L. Özcep</b></p>
	Achim D. Brucker and Amy Stell
	<b>Verifying Feedforward Neural Networks for Classification in Isabelle/HOL</b>
11:00-12:30	Nicolas Amat and Silvano Dal Zilio
	<b>SMPT: A Testbed for Reachability Methods in Generalized Petri Nets</b>
	Taylor Dohmen, Stanley Bak, Ashutosh Trivedi, Alvaro Velasquez, Piotr Wojciechowski and K. Subramani
	<b>The Octatope Abstract Domain for Verification of Neural Networks</b>
	Fortunat Rajaona, Ioana Boureau, Vadim Malvone and Francesco Belardinelli
	<b>Program Semantics and Verification Technique for AI-centred Programs *</b>
12:30-14:00	<b>Lunch</b>
14:00-15:00	<p align="center"><b>Luminary Talk - Jeanette Wing</b> <b>Trustworthy AI</b> Session Chair: <b>Marsha Chechik</b></p>
15:00-15:30	<p align="center"><b>Journal First Track - Mario Gleirscher, Radu Calinescu, James Douthwaite, Benjamin Lesage, Colin Paterson, Jonathan Aitken, Rob Alexander and James Law</b> <b>Verified Synthesis of Optimal Safety Controllers for Human-Robot Collaboration</b> Session Chair: <b>Marsha Chechik</b></p>
15:30-16:00	<b>Coffee Break</b>
	<p align="center">Session: <b>Safety and Reliability</b> Session Chair: <b>Jan O. Ringert</b></p>
16:00-17:30	Montserrat Hermo, Paqui Lucio and Cesar Sanchez
	<b>Tableaux for Realizability of Safety Specifications</b>
	Sebastiaan Brand, Thomas Bäck and Alfons Laarman
	<b>A Decision Diagram Operation for Reachability *</b>
	Tsutomu Kobayashi, Martin Bondu and Fuyuki Ishikawa
	<b>Formal Modelling of Safety Architecture for Responsibility-Aware Autonomous Vehicle via Event-B Refinement</b>
	Davide Basile and Maurice H. ter Beek
	<b>A Runtime Environment for Contract Automata</b>
17:30-18:00	<b>Closing Session - Announcement of FM24 - Session Chair: Martin Leucker</b>

# Friday, March 10th

	<p><b>WORKSHOP - Overture</b> Room: <a href="#">AM S2</a> Organizers: Hugo Daniel Macedo, Ken Pierce</p>
09:00-10:30	<p><b>Session 1</b> Session Chair: <b>Hugo Daniel Macedo</b></p>
	<p><b>Welcome from the chairs</b></p>
	<p><b>International System of Quantities library in VDM</b> (Leo Freitas)</p>
	<p><b>Implementation-First Approach of Developing Formal Semantics of a Simulation Language in VDM-SL</b> (Tomohiro Oda, Gael Dur, Stephane Ducasse and Hugo Macedo)</p>
10:30-11:00	<p><b>Coffee Break</b></p>
11:00-12:30	<p><b>Session 2</b> Session Chair: <b>Ken Pierce</b></p>
	<p><b>Specification-based CSV support in VDM</b> (Leo Freitas and Aaron John Buhagiar)</p>
	<p><b>VDM recursive functions in Isabelle/HOL</b> Leo Freitas and Peter Gorm Larsen</p>
	<p><b>Topologically sorting VDM-SL definitions for Isabelle/HOL translation</b> Leo Freitas and Nick Battle</p>
12:30-14:00	<p><b>Lunch</b></p>
14:00-15:30	<p><b>Session 3</b> Session Chair: <b>Nick Battle</b></p>
	<p><b>Bidirectional UML Visualisation of VDM Models</b> (Jonas Lund, Lucas Bjarke Jensen, Peter Gorm Larsen and Hugo Daniel Macedo)</p>
	<p><b>Modelling Chess in VDM++</b> (Morten Haahr Kristensen and Peter Gorm Larsen)</p>
	<p><b>Modelling Maritime SAR Sweep Widths for Helicopters in VDM</b> (Alexander Sulaiman and Ken Pierce)</p>
	<p><b>Community status and open table discussion</b></p>
	<p><b>End of the Event</b></p>